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Bescheinigung

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Attestation

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The attached documents are exact copies of the European patent application described on the following page, as originally filed.

Les documents fixés à cette attestation sont conformes à la version initialement déposée de la demande de brevet européen spécifiée à la page suivante.

Patentanmeldung Nr. Patent application No. Demande de brevet n°

98402408.3

Der Präsident des Europäischen Patentamts;
Im Auftrag

For the President of the European Patent Office

Le Président de l'Office européen des brevets
p.o.

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Page 2 de l'attestation

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Method and arrangements for transition between a low power state and a full power state in a communication system

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**METHOD AND ARRANGEMENTS FOR TRANSITION BETWEEN A LOW
POWER STATE AND A FULL POWER STATE IN A COMMUNICATION
SYSTEM**

5 The present invention relates to a method to transit in a communication system from a low power state to a full power state as defined in the non-characteristic part of claim 1, a method to transit in a communication system from a full power state to a low power state as defined in the non-characteristic part of claim 9, state transition arrangements to be used in a transmitter to carry
10 out steps of these methods as defined in the non-characteristic parts of claims 5 and 10 respectively, state transition arrangements to be used in a receiver to carry out steps of these methods as defined in the non-characteristic parts of claims 6 and 11 respectively, and communication systems comprising a transmitter and receiver including such state transition arrangements as defined
15 in the non-characteristic parts of claims 8 and 12 respectively.

Such a state transition method and arrangements to perform such a method are already known in the art, e.g. from the temporary document WH-031 submitted on June 29, 1998 to ITU Study Group 15 which is an ADSL (Asymmetric Digital Subscriber Line) standardisation forum. This document WH-
20 031 is entitled 'Time Domain Rate Adaptation Based L1 State for G.Lite Modem Power Down Management' and originates from ITeX. Therein, a mechanism for transition from the L1 state, a low power/low bit rate state, to an L0 state, a full power/full bit rate state, of an ADSL (Asymmetric Digital Subscriber Line) modem is described. As indicated in paragraph 3 of the cited document, transition from
25 the low power to the full power state requires transmission of a predetermined recognisable state transition indication, called EOC message. Such an EOC message is also transferred between transmitter and receiver to initiate transition from the full power state to the low power state, as can be derived from paragraph 2 of the mentioned ITU submission. Due to the requirement to send
30 such a predetermined state transition indication, the transition time to switch from

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the low power state to the full power state or vice versa is not minimised in the known solution. In particular in communication systems with buffers temporarily storing data, larger state transition times imply increased probability for buffer overflow, congestion and even loss of data. If for instance ATM (Asynchronous Transfer Mode) cells have to be transferred over an ADSL (Asymmetric Digital Subscriber Line) network segment, risk for ATM buffer overflow increases if the wake-up time, i.e. the transition time from the low power state to the full power state, of the ADSL network segment is large. As will be explained later in this application, this drawback of the solution known from the cited document is even more dramatic in case data symbols are transferred at a low bit rate during the low power state.

An object of the present invention is to provide a method and arrangements for transition between a low power state and a full power state similar to the known one, but wherein the transition time for switching between the full power state and low power state is minimised.

According to the invention, this object is achieved by the method to transit in a communication system from a low power state to a full power state as defined in claim 1, the method to transit in a communication system from a full power state to a low power state as defined in claim 9, the state transition arrangements to be used in a transmitter to carry out steps of these methods as defined in claims 5 and 10 respectively, the state transition arrangements to be used in a receiver to carry out steps of these methods as defined in claims 6 and 11 respectively, and the communication systems comprising a transmitter and receiver including such state transition arrangements as defined in claims 8 and 12 respectively.

Indeed, provided that the receiver is able to discriminate a data symbol transmitted at full power from a data symbol transmitted at low power, the transition from the low power state to the full power state can be initiated by transmitting a data symbol at full power without first transmitting a predetermined state transition indication. In this way, the state transition time is

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reduced with the transmission time for the predetermined indication used in the known solution. Similarly, again provided that the receiver is able to discriminate a data symbol transmitted at full power from a data symbol transmitted at low power, the transition from the full power state to the low power state can be initiated by transmitting a data symbol at low power without first transmitting a predetermined state transition indication. It is however noticed that for the transition from the full power state to the low power state, the duration of the transition is less critical because no data can get lost due to buffer congestion in this situation.

Summarising, the data symbol that indicates the transition from the low power state to the full power state or vice versa, according to the present invention carries user data that may be further processed by the receiver instead of having a predetermined contents. An advantageous consequence thereof is that the communication system is faster operating in the new state.

It is to be noticed that the term 'comprising', used in the claims, should not be interpreted as being limitative to the means listed thereafter. Thus, the scope of the expression 'a device comprising means A and B' should not be limited to devices consisting only of components A and B. It means that with respect to the present invention, the only relevant components of the device are A and B.

Similarly, it is to be noticed that the term 'coupled', also used in the claims, should not be interpreted as being limitative to direct connections only. Thus, the scope of the expression 'a device A coupled to a device B' should not be limited to devices or systems wherein an output of device A is directly connected to an input of device B. It means that there exists a path between an output of A and an input of B which may be a path including other devices or means.

An additional feature of the method to transit from a low power state to a full power state according to the present invention is defined by claim 2.

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Thus, a possible way to discriminate in the receiver between data symbols transmitted at low power and data symbols transmitted at full power is by carrying out quality measurements on the received data symbols, such as for instance signal-to-noise ratio (SNR) measurements. Indeed, for a data symbol
5 sent at full power, the signal level will be significantly higher than the noise level so that the SNR measurement allows to detect the full power data symbol.

Another feature of the method according to the present invention is defined by claim 3 and claim 7.

Thus, the data symbol sent at full power, indicating the transition from the
10 low power state to the full power state, is processed by the receiver only if its quality exceeds a certain threshold. If the reception quality does not exceed this threshold, the receiver accumulates the data symbol but does not process the data symbol to demodulate information therefrom.

Furthermore, an additional feature of the method according to the present
15 invention is defined by claim 4.

Indeed, in case the data symbols are sent at a low bit rate during the low power state, the transition time from the low power state to the full power state is even more reduced if transmission of a full power data symbol without previous transmission of a predetermined state transition indication is combined with
20 interruption of the actually transmitted low power data symbol. This is so because according to the known method, the transmitter has to wait until the end of the low power data symbol before it can indicate the transition to the full power state, which may cost several data symbol times because of the low bit rate at which the low power data symbol is transferred. Evidently, the full power data symbol
25 according to the present invention transferred between the transmitter and receiver to initiate the transition from the low power state to the full power state has to be a copy of the interrupted low power data symbol unless information gets lost.

The above mentioned and other objects and features of the invention will
30 become more apparent and the invention itself will be best understood by

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referring to the following description of an embodiment taken in conjunction with the accompanying drawings wherein:

Fig. 1 is a block scheme of a communication system with a transmitter TX and receiver RX including state transition arrangements according to the present invention;

Fig. 2 is a data symbol flow diagram illustrating transition from the low power state LPS to the full power state FPS in a first embodiment of the known method;

Fig. 3 is a data symbol flow diagram illustrating transition from the low power state LPS to the full power state FPS in a first embodiment of the method according to the present invention;

Fig. 4 is a data symbol flow diagram illustrating transition from the low power state LPS to the full power state FPS in a second embodiment of the known method, wherein the low power state LPS corresponds to a low bit rate state; and

Fig. 5 is a data symbol flow diagram illustrating transition from the low power state LPS to the full power state FPS in a second embodiment of the method according to the present invention, wherein the low power state LPS corresponds to a low bit rate state.

In the communication system of Fig. 1, a transmitter TX is coupled to a receiver RX via a communication medium CM. The transmitter TX includes transmission means TXM, activity detection means ADM and state transition means TRM. The receiver RX contains reception means RXM, quality measurement means QM, a comparator C, a threshold value generator T, state transition means TRM', and accumulation means ACM.

In the transmitter TX, the transmission means TXM are coupled between a data input terminal and a data symbol output terminal of the transmitter TX. The activity detection means ADM and the transition means TRM are cascade coupled between the data input terminal of the transmitter TX and a state transition control terminal of the transmission means TXM. In the receiver RX, the reception means are coupled between a data symbol input terminal and a data output

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terminal of the receiver RX. The quality measurement means QM is coupled between the just mentioned data symbol input terminal of the receiver RX and a first input terminal of the comparator C. To a second input terminal of the comparator C, an output of the threshold value generator T is coupled. An output terminal of the comparator C further is connected to input terminals of both the transition means TRM' and the accumulator means ACM. The transition means TRM' has an output terminal interconnected with a state transition control terminal of the reception means RXM, and the accumulation means ACM is provided with an output terminal interconnected with a process control terminal of the reception means RXM. The communication medium CM in Fig. 1, which is supposed to be a twisted pair copper telephone wire, interconnects the data symbol output terminal of the transmitter TX, which is supposed to be a DMT (Discrete Multi Tone) transmitter operating according to the ADSL (Asymmetric Digital Subscriber Line) physical layer protocol, and the data symbol input terminal of the receiver RX, which consequently is supposed to be a DMT (Discrete Multi Tone) receiver operating according to the ADSL (Asymmetric Digital Subscriber Line) physical layer protocol. The data symbols transferred between the transmitter TX and receiver RX are discrete multi tone (DMT) symbols.

The operation of the different functional blocks of the transmitter TX and receiver RX in Fig. 1 is best described with reference to Fig. 3 and Fig. 5. To illustrate the difference with the prior art system, Fig. 2 and Fig. 4 are inserted.

In Fig. 2 and Fig. 3, an embodiment of the invention is considered wherein the transmitter TX and receiver RX both can operate in a low power state LPS and a full power state FPS, but the bit rate remains the same in both states. The data symbols DMT10, DMT11, DMT20 and DMT21, transmitted at low power in Fig. 2 and Fig. 3 consequently have the same length in time as the data symbols DMT12 and DMT22 transmitted at full power. In the prior art system illustrated by Fig. 2 as well as the new system illustrated by Fig. 3, the transmitter TX and receiver RX are operating in the low power state LPS until the activity detection means ADM in the transmitter TX detect data that need to be

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transmitted towards the receiver RX at the data input terminal of the transmitter TX. The point in time of activity detection is marked by AD in Fig. 2 and Fig. 3. The activity detection means ADM informs the transition means TRM that data have to be transmitted and the latter transition means TRM generates a control signal indicating that the transmission means TXM has to transit from the low power state LPS to the full power state FPS, and applies this control signal to the state transition control terminal of the transmission means TXM. In the prior art system of Fig. 2, the transmission means waits until the end of the currently transmitted data symbol DMT11, thereupon transmits a symbol PS with predetermined contents, and afterwards sends a first data symbol DMT12 at full power. A receiver, capable to communicate with such a known transmitter monitors the communication channel CM until it recognises the predetermined symbol PS and thereupon transits to the full power state FPS wherein it is able to receive the full power data symbol DMT12 and to demodulate data therefrom.

15 The time required for transmission of the predetermined symbol PS in Fig. 2 is used more efficiently in Fig. 3 so that the transition time to the full power state FPS is shortened. Upon detection of activity at the data input terminal of the transmitter TX, the activity detection means ADM and the transition means TRM again control the transmission means TXM to switch from the low power state LPS to the full power state FPS, but the transmission means TXM now is no longer enabled to transmit a symbol PS with predetermined contents but immediately sends a data symbol DMT22 at full power. The receiver RX, capable to communicate with this transmitter TX, includes means that can discriminate between a low power data symbol and a full power data symbol. In Fig. 1, the receiver RX thereto is equipped with the quality measurement means QM which continuously measure the signal-to-noise ratio (SNR) of the incoming signal. The comparator C activates the transition means TRM' in case the measured signal-to-noise ratio (SNR) exceeds a threshold value generated by the threshold value generator T, or activates the accumulation means ACM in case the measured signal-to-noise ratio (SNR) does not exceed this threshold value. Upon receipt of

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- the low power data symbols DMT20 and DMT21 by the receiver RX, the accumulation means ACM is thus activated and controls the reception means RXM to accumulate the received symbols instead of processing them and demodulating information therefrom. Upon arrival of the full power data symbol
- 5 DMT22, the comparator C de-activates the accumulation means ACM and activates the transition means TRM'. The latter transition means TRM' controls the reception means RXM to switch from the low power state LPS to the full power state FPS and to process the incoming data symbol DMT22 instead of accumulating it.
- 10 In Fig. 4 and Fig. 5, communication systems are considered wherein the low power state LPS corresponds to a low bit rate state. This means that data symbols DMT31, DMT41 transferred at low power are also transferred at a low bit rate compared to the full bit rate at which the data symbols DMT32, C_DMT41 and DMT42 are transferred during the full power state FPS. Data
- 15 symbols DMT31 and DMT41 transferred in the low power state LPS consequently are longer in time than data symbols DMT32, C_DMT41 and DMT42 transferred at full power. In the prior art system, whose working is illustrated by Fig. 4, the transition from the low power state LPS to the full power state FPS is not only delayed by transmission of a symbol PS with predetermined contents, but also by
- 20 waiting for the end boundary of the currently transmitted data symbol DMT31, from the moment on activity is detected at the input of the transmitter TX. This moment is indicated by AD in Fig. 4. In the system according to the present invention, illustrated by Fig. 5, transition from the low power state LPS to the full power state FPS is speed up by immediately sending a data symbol C_DMT41
- 25 without previous transmission of a predetermined symbol PS, similar to the above described embodiment. Moreover, the transition to the full power state FPS in Fig. 5 is speed up by interrupting transmission of the currently transmitted data symbol DMT41 at the moment activity is detected at the input of the transmitter TX. A copy C_DMT41 of the interrupted data symbol DMT41 is integrally
- 30 transmitted at full power and full bit rate at the beginning of the full power state

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FPS to avoid loss of information. Nevertheless, the average transition time to go from the low power state LPS to the full power state FPS is reduced provided that the bit rate is decreased by a factor higher than 2 in the low power state LPS. Indeed, in that case, the copy C_DMT41 of the currently transmitted data symbol DMT41 will be received in more than 50 % of the transitions from the low power state LPS to the full power state FPS earlier than the original data symbol DMT41 if it would have been transmitted completely. Summarising, interruption of the currently transmitted low power data symbol DMT41 even more speeds up transition from the low power state LPS to the full power state FPS in communication systems wherein data symbols are transmitted in the low power state LPS at a bit rate which is at least a factor 2 smaller than the bit rate at which data symbols are transferred during the full power state FPS.

The principles described in the foregoing paragraphs to reduce the transition time to go from the low power state LPS to the full power state FPS, for evident reasons are also applicable to the transition from the full power state FPS to the low power state LPS. A person skilled in the art however will appreciate that for the transition from the full power state FPS to the low power state LPS, the transition time is less critical because a longer transition time there cannot lead to loss of information, congestion of buffers, or the like. A reduction of the transition time to go from the full power state FPS to the low power state LPS nevertheless has an effect on the consumption of power in the communication system and for this reason is still advantageous.

A first remark is that, although the data symbols in the above described embodiment may be transported over a telephone line like in ADSL (Asymmetric Digital Subscriber Lines) or VDSL (Very High Speed Digital Subscriber Lines) systems, the applicability of the present invention is not restricted by the transmission medium via which the signal is transported. In particular, the present invention is applicable to connections over a cable, an optical fiber, a satellite link, a radio link through the air, and so on, between a transmitter and receiver that can be operating in a full power state and low power state.

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The invention also is not only related to systems wherein the ADSL (Asymmetric Digital Subscriber Line) or any similar physical layer protocol is used. A person skilled in the art will be able to adapt the above described embodiment so that it is applicable in any other system wherein a transmitting modem and
5 receiving modem have full power and low power states.

Another remark is that embodiments of the present invention are described above in terms of functional blocks. From the functional description of these blocks it will be obvious for a person skilled in the art of designing electronic devices how embodiments of these blocks can be manufactured with
10 well-known electronic components. A detailed architecture of the contents of the functional blocks hence is not given for most of them.

While the principles of the invention have been described above in connection with specific apparatus, it is to be clearly understood that this description is made only by way of example and not as a limitation on the scope
15 of the invention.

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CLAIMS

1. Method to transit in a communication system comprising a transmitter (TX), a communication medium (CM) and a receiver (RX), from a low power state (LPS) wherein data symbols (DMT10, DMT11, DMT20, DMT21, DMT31, DMT41) are transferred between said transmitter (TX) and said receiver (RX) at low power to a full power state (FPS) wherein data symbols (DMT12, DMT22, DMT32, C_DMT41, DMT42) are transferred between said transmitter (TX) and said receiver (RX) at full power, said method including the steps of:
- a. detecting activity in said transmitter (TX) during said low power state (LPS); and
 - b. transiting from said low power state (LPS) to said full power state (FPS) in said transmitter (TX),
- CHARACTERISED IN THAT said method further comprises the steps of:
- c. transmitting a data symbol (DMT22, C_DMT41) at full power from said transmitter (TX) to said receiver (RX) without first transmitting a predetermined state transition indication (PS);
 - d. detecting in said receiver (RX) said data symbol (DMT22, C_DMT41) transmitted at full power; and
 - e. transiting from said low power state (LPS) to said full power state (FPS) in said receiver (RX).
2. Method according to claim 1,
- CHARACTERISED IN THAT said step of detecting in said receiver (RX) said data symbol (DMT22, C_DMT41) transmitted at full power is performed by:
- d1. measuring a quality of said data symbol (DMT22, C_DMT41) in said receiver (RX); and

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d2. comparing said quality of said data symbol (DMT22, C_DMT41) with a predetermined threshold (T).

3. Method according to claim 2,

- 5 CHARACTERISED IN THAT said method further comprises the step of:
- e1. transiting from said low power state (LPS) to said full power state (FPS) in said receiver (RX) and processing said data symbol (DMT22, C_DMT41) in case said quality exceeds said predetermined threshold (T); or
 - e2. staying in said low power state (LPS) in said receiver (RX) and
- 10 accumulating said data symbol (DMT22, C_DMT41) in case said quality does not exceed said predetermined threshold (T).

4. Method according to any of claims 1 to 3,

- 15 CHARACTERISED IN THAT during said low power state (LPS) data symbols (DMT31, DMT41) are transferred between said transmitter (TX) and said receiver (RX) at a low bit rate, and during said full power state (FPS) data symbols (DMT32, C_DMT41, DMT42) are transferred between said transmitter (TX) and said receiver (RX) at full bit rate, and in that said data symbol (C_DMT41) sent at full power is a copy of a previously transmitted
- 20 data symbol (DMT41) whose transmission at low power was interrupted by said transmitter (TX) upon detection of said activity.

5. State transition arrangement to be used in a transmitter (TX) being adapted to transmit data symbols (DMT10, DMT11, DMT20, DMT21,
- 25 DMT31, DMT41) at low power when it is operating in a low power state (LPS) and to transmit data symbols (DMT12, DMT22, DMT32, C_DMT41, DMT42) at full power when it is operating in a full power state (FPS), said arrangement comprising:

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a. transmission means (TXM), coupled between an input terminal and an output terminal of said transmitter (TX) and adapted to embed incoming data in data symbols for transmission over a communication medium (CM);

5 b. activity detection means (ADM), coupled to said input terminal of said transmitter (TX) and adapted to detect activity when said transmission means (TXM) is operating in said low power state (LPS); and

c. transition means (TRM), coupled between an output of said activity detection means (ADM) and a state control input of said transmission means (TXM), and adapted to bring said transmission means (TXM) from
10 said low power state (LPS) into said full power state (FPS),

CHARACTERISED IN THAT said transmission means (TXM), when brought into said full power state (FPS) under control of said transition means (TRM), is adapted to transmit a data symbol (DMT22, C_DMT41) at full power without first sending a predetermined state transition indication
15 (PS).

6. State transition arrangement to be used in a receiver (RX) being adapted to receive data symbols (DMT10, DMT11, DMT20, DMT21, DMT31, DMT41) at low power when it is operating in a low power state
20 (LPS) and to receive data symbols (DMT12, DMT22, DMT32, C_DMT41, DMT42) at full power when it is operating in a full power state (FPS), said arrangement comprising:

a. reception means (RXM) coupled between an input terminal and an output terminal of said receiver (RX) and adapted to receive incoming data
25 symbols transferred over a communication medium (CM),

CHARACTERISED IN THAT said arrangement further comprises:

b. detection means (QM, C, T), coupled to said input terminal of said receiver (RX) and adapted to detect a data symbol (DMT22, C_DMT41) transmitted at full power without previous detection of a predetermined state

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transition indication (PS) when said reception means (RXM) is operating in said low power state (LPS); and

5 c. transition means (TRM'), coupled to said detection means (C) and adapted to bring said reception means (RXM) from said low power state (LPS) into said full power state (FPS), upon detection of said data symbol (DMT22, C_DMT41) transmitted at full power.

7. State transition arrangement according to claim 6,

CHARACTERISED IN THAT said detection means (QM, C, T)

10 comprises:

b1. quality measurements means (QM), adapted to measure a quality of said data symbol (DMT22, C_DMT41) transmitted at full power; and

15 b2. comparison means (C), coupled to said quality measurement means (QM) and adapted to compare said quality with a predetermined threshold value (T), and further in that:

c1. said transition means (TRM') is adapted to be activated by said comparison means (C) in case said quality exceeds said predetermined threshold (T), and further in that said arrangement comprises:

20 d. accumulation means (ACM), coupled to said comparison means (C) and adapted to accumulate said data symbol (DMT22, C_DMT41) transmitted at full power, said accumulation means (ACM) being adapted to be activated by said comparison means (C) in case said quality does not exceed said predetermined threshold (T).

25 8. Communication system comprising a transmitter (TX), a communication medium (CM) and a receiver (RX), wherein data symbols (DMT10, DMT11, DMT20, DMT21, DMT31, DMT41) are transferred between said transmitter (TX) and said receiver (RX) at low power in a low power state (LPS) and wherein data symbols (DMT12, DMT22, DMT32,

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C_DMT41, DMT42) are transferred between said transmitter (TX) and said receiver (RX) at full power in a full power state (FPS),

CHARACTERISED IN THAT said transmitter (TX) comprises a state transition arrangement as defined in claim 5 and said receiver (RX) comprises a state transition arrangement as defined in claim 6 or claim 7.

9. Method to transit in a communication system comprising a transmitter (TX), a communication medium (CM) and a receiver (RX), from a full power state (FPS) wherein data symbols are transferred between said transmitter (TX) and said receiver (RX) at full power to a low power state (LPS) wherein data symbols are transferred between said transmitter (TX) and said receiver (RX) at low power, said method including the steps of:

- a. detecting non-activity in said transmitter (TX) during said full power state (FPS); and
- b. transiting from said full power state (FPS) to said low power state (LPS) in said transmitter (TX),

CHARACTERISED IN THAT said method further comprises the steps of:

- c. transmitting a data symbol at low power from said transmitter (TX) to said receiver (RX) without first transmitting a predetermined state transition indication (PS);
- d. detecting in said receiver (RX) said data symbol transmitted at low power; and
- e. transiting from said full power state (FPS) to said low power state (LPS) in said receiver (RX).

10. State transition arrangement to be used in a transmitter (TX) being adapted to transmit data symbols at low power when it is operating in a low power state (LPS) and to transmit data symbols at full power when it is operating in a full power state (FPS), said arrangement comprising:

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a. transmission means (TXM), coupled between an input terminal and an output terminal of said transmitter (TX) and adapted to embed incoming data in data symbols for transmission over a communication medium (CM);

5 b. activity detection means (ADM), coupled to said input terminal of said transmitter (TX) and adapted to detect non-activity when said transmission means (TXM) is operating in said full power state (FPS); and

c. transition means (TRM), coupled between an output of said activity detection means (ADM) and a state control input of said transmission means (TXM), and adapted to bring said transmission means (TXM) from
10 said full power state (FPS) into said low power state (LPS),

CHARACTERISED IN THAT said transmission means (TXM), when brought into said low power state (LPS) under control of said transition means (TRM), is adapted to transmit a data symbol at low power without first sending a predetermined state transition indication (PS).

15

11. State transition arrangement to be used in a receiver (RX) being adapted to receive data symbols at low power when it is operating in a low power state (LPS) and to receive data symbols at full power when it is operating in a full power state (FPS), said arrangement comprising:

20 a. reception means (RXM) coupled between an input terminal and an output terminal of said receiver (RX) and adapted to receive incoming data symbols transferred over a communication medium (CM),

CHARACTERISED IN THAT said arrangement further comprises:

b. detection means (QM, C, T), coupled to said input terminal of said
25 receiver (RX) and adapted to detect a data symbol transmitted at low power without previous detection of a predetermined state transition indication (PS) when said reception means (RXM) is operating in said full power state (FPS); and

c. transition means (TRM'), coupled to said detection means (C) and
30 adapted to bring said reception means (RXM) from said full power state

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(FPS) into said low power state (LPS), upon detection of said data symbol transmitted at low power.

12. Communication system comprising a transmitter (TX), a
5 communication medium (CM) and a receiver (RX), wherein data are
transferred between said transmitter (TX) and said receiver (RX) at low power
in a low power state (LPS) and wherein data symbols are transferred
between said transmitter (TX) and said receiver (RX) at full power in a full
power state (FPS),

10 CHARACTERISED IN THAT said transmitter (TX) comprises a state
transition arrangement as defined in claim 10 and said receiver (RX)
comprises a state transition arrangement as defined in claim 11.

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ABSTRACT**METHOD AND ARRANGEMENTS FOR TRANSITION BETWEEN A LOW
POWER STATE AND A FULL POWER STATE IN A COMMUNICATION
SYSTEM**

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10 In a communication system wherein data symbols (DMT10, DMT11, DMT20, DMT21, DMT31, DMT41) are transferred between a transmitter (TX) and receiver (RX) at low power during a low power state (LPS) and wherein data symbols (DMT12, DMT22, DMT32, C_DMT41, DMT42) are transferred between the transmitter (TX) and receiver (RX) at full power during a full power state (FPS), fast transition from the low power state (LPS) to the full power state (FPS) is realised by sending a data symbol (DMT22, C_DMT41) at full power from the transmitter (TX) to the receiver (RX) without first sending a predetermined state transition indication (PS). Vice versa, a data symbol is transmitted from the transmitter (TX) to the receiver (RX) at low power, not preceded by a predetermined state transition indication (PS) to realise fast transition from the full power state (FPS) to the low power state (LPS).

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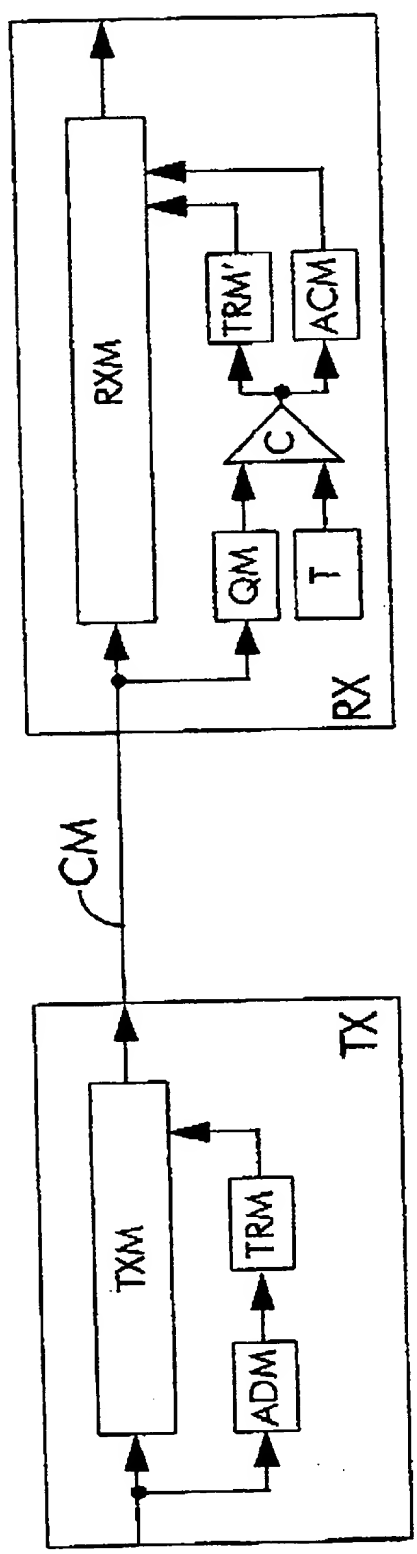


Fig. 1

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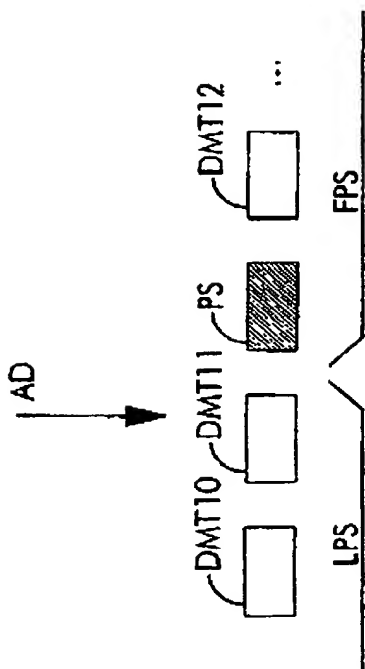


Fig. 2 (Prior Art)

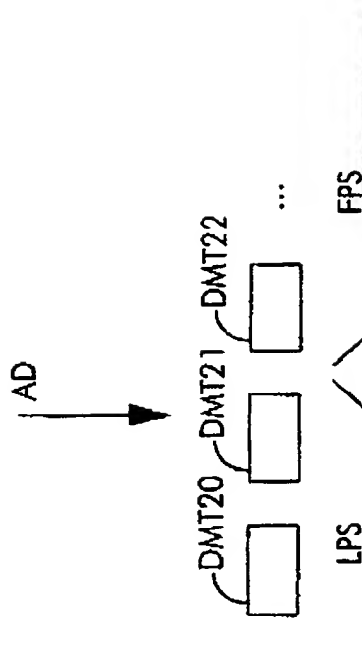


Fig. 3

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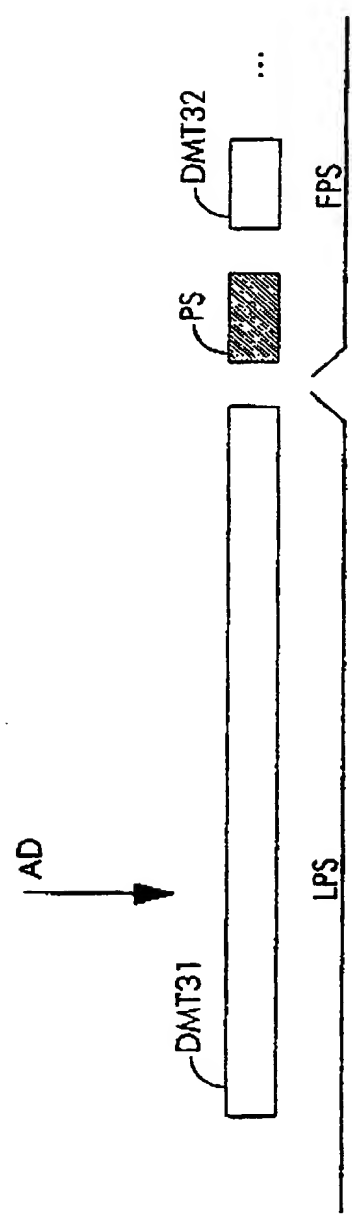


Fig. 4 (Prior Art)

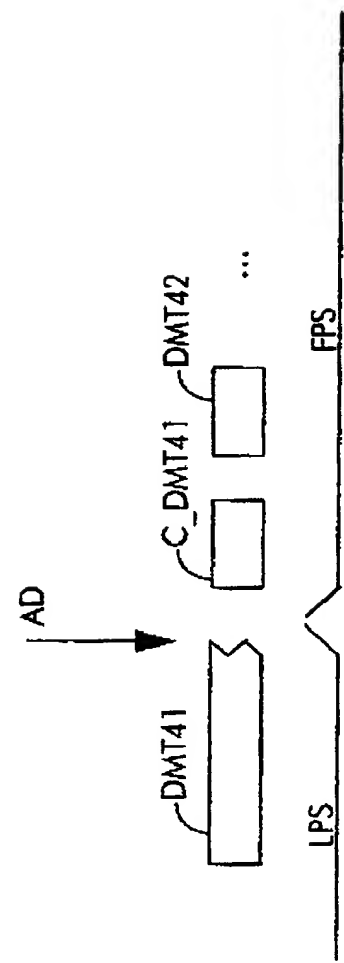


Fig. 5

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